

**ON THE DESIGN OF LOW-VOLTAGE, VOLTAGE MODE MINIMUM
AND MAXIMUM CIRCUIT WITH THRESHOLD VOLTAGE
CANCELLATION IN CMOS TECHNOLOGY**

SURACHAI CHUNCHAY

**A THESIS SUBMITTED IN PARTIAL FULFILLMENT
OF THE REQUIREMENTS FOR DEGREE OF
MASTER OF ENGINEERING IN TELECOMMUNICATION ENGINEERING
SCHOOL OF GRADUATE STUDIES
KING MONGKUT'S INSTITUTE OF TECHNOLOGY LADKRABANG**

2007