



**THESIS APPROVAL**  
**GRADUATE SCHOOL, KASETSART UNIVERSITY**

Master of Engineering (Electrical Engineering)  
**DEGREE**

Electrical Engineering  
**FIELD**

Electrical Engineering  
**DEPARTMENT**

**TITLE:** Sidelobe Reduction in Peano-Gosper Fractal Arrays using Genetic Algorithms

**NAME:** Mr. Meta Polpasee

**THIS THESIS HAS BEEN ACCEPTED BY**

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**APPROVED BY THE GRADUATE SCHOOL ON** 21 / 03 / 07

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