

Thesis Title	Digital PLL Frequency Synthesizer and Its Applications in Communicatoin Systems
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ABSTRACT

Phase locked loops are widely used in control system and communication system, expecially for frequency synthesizer in various frequency ranges. This thesis proposes the uses of digital phase-locked loop to apply in digital communication systems, the proposed applications have many types but also to apply with frequency synthesizer. The experimental results and simulation results are proposed with the comparison to all of the results.