

## BIOGRAPHY

<b>Name – Surname</b>	Mr. Peerasak Mutuwong
<b>Date of Birth</b>	19 June 1985
<b>Education Background</b>	
High school	High School in Science-Math, Ratchaprachasamasai School, Samutprakarn 2004
Bachelor's degree	Bachelors of Engineering (Electrical Engineering), Suranaree University of Technology (SUT), Nakhonratchasima, 2008
Master's degree	Master of Engineering (Electrical and Information Engineering), King Mongkut's University of Technology Thonburi, 2014
<b>Work Experiences</b>	
2009-2011	Maintenance Engineer Sammitr metal work Co., Ltd. Samutprakarn
2011-2014	Maintenance Engineer Bangkok Spring Industrial Co., Ltd. Samutprakarn
2014-present	Electrical Engineer Perfect companion group Co., Ltd. Samutprakarn
<b>Publication</b>	Mutuwong, P. Assawinchaichote, W., 2014 “The programmable logic controller kit for test devices about automation system”, <b>The 4<sup>th</sup> STOU Graduate Research Conference</b> , 26-27 November 2014, Sukhothai Thammathirat Open University

